	Туре	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	0	(FET or Field near effect near transistor) and (high near power) and (channel near regions) and (perpendicular near width) and (bent near agte near electro	USPAT; JPO; DERWE	2001/08/1 3 14:56
2	BRS	L2	0	(FET or Field near effect near transistor) and (high near power) and (channel near regions) and (bent near agte near electrodes)	USPAT; JPO; DERWE NT	2001/08/1 3 15:06
3	BRS	L3	0	(FET or Field near effect near transistor) and (high near power) and (bent near gate near	USPAT; JPO; DERWE NT	2001/08/1 3 15:00
4	BRS	L4	1	effect near transistor) and (bent near gate near	USPAT; ; JPO; DERWE NT	2001/08/1 3 15:06
5	BRS	L5		effect near transistor) and (high near power) and (USPAT; JPO; DERWE NT	2001/08/1 3 15:08
6	BRS	L6	4	effect near transistor) and (high near power) and (USPAT; JPO; DERWE NT	2001/08/1 3 15:08